No. 3231C

LA1851N

DTS Single-chip Tuner IC

Overview

The LA1851N is a tuner IC designed for home-use stereo equipment which supports the SD system and IF counter system and incorporates AM/FM IF/MPX functions on a single chip.

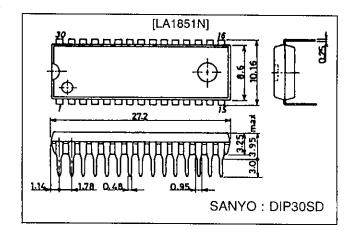
Features

- * AM/FM tuner and multiplex demodulator on a single chip
- · No multiplexer adjustments required
- Output (FM/AM) for IF counter buffer compatible with electronic tuning
- · Stereo separation control
- Forced monaural and VCO stop functions
- · Minimal multiplexer carrier leakage
- Excellent VCO temperature characteristics: $f_o = 0.1\%$ typ. with ± 50 deg. variation

Package Dimensions

unit: mm

3196-DIP30SD



Functions

[FM Block]

- IF amplifier
- · Quadrature detector
- · IF counter buffer

- S-meter output
- · Tuner indicator (variable sensitivity)

[AM Block]

- RF amplifier
- Mixer

- Oscillator
- Oscillator buffer

- IF amplifier
- Detector
- AGC

- IF counter buffer
- Tuner indicator (variable sensitivity)

[MPX Block]

- PLL decoder
- · ST indicator
- VCO stop
- Mute

- Separation control
- No VCO adjustment
- Forced monaural (VCO stop)

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max	Pins 3, 7, 10, 21, 22 and 26	14	V
Maximum supply current	I _{CC} max	Pin 3	40	mA
		Pins 21 and 22	20	mA
Allowable power dissipation	Pd max	Ta = 70°C	480	mW
Operating temperature	Topr		-20 to +70	°C
Storage temperature	Tstg		-40 to +125	°C

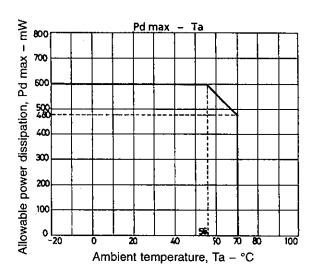
Operating Conditions at Ta = 25 $^{\circ}$ C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V _{CC}	_	8.5	V
Operating supply voltage range	V _{CC} op		6 to 12	V

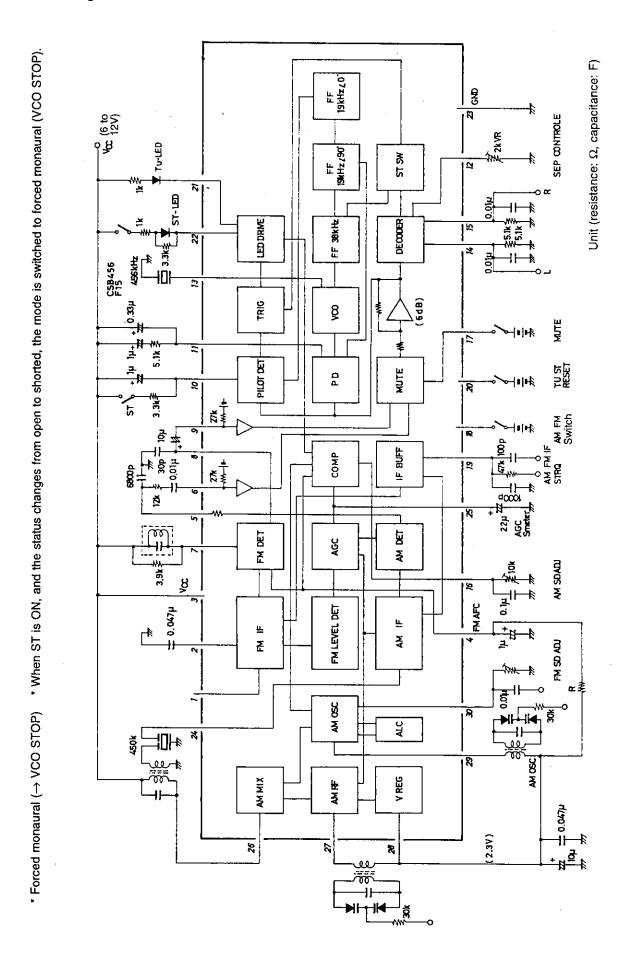
Operating Characteristics at Ta = 25°C, $V_{\rm CC}$ = 8.5 V

Parameter	Symbol	Conditions	min	typ	max	Unit	
[AM: fin = 1 MHz, 1 kHz tone]							
Quiescent current	lcco	No input		26	37	mA	
Datasta- autout	V _O 1	V _{IN} = 23 dBμ, 30% AM	70	140	260	mV	
Detector output	V _O 2	V _{IN} =80 dBµ, 30% AM	170	280	390	mV	
Singal-to-nose ratio	S/N1	V _{IN} = 23 dBμ, 30% AM	15	19		d₿	
Singal-to-nose ratio	S/N2	V _{IN} = 80 dBμ, 30% AM	45	50		dB	
Total harmonic distortion	THD1	V _{IN} = 80 dBμ, 30% AM		0.5	1.2	%	
Total Halffloric distortion	THD2	V _{IN} = 100 dBμ, 30% AM		0.6	1.3	%	
IF buffer output	V _{IF}	V _{IN} = 20 dBµ	110	170	230	mV	
Local oscillator buffer output	Vosc	f _{OSC} = 1.450 MHz	290	350	420	mV	
Tuner turn-on sensitivity	٧s	Variable sensitivity		(13)		dΒμ	
[FM mono: $f_{in} = 10.7 \text{ MHz}$, 1 kHz	tone]			· · · · · · · · · · · · · · · · · · ·		· · · · · · · · · · · · · · · · · · ·	
Quiesect current	lcco	No input		27	38	mA	
Input limiting sensitivity	LMS	3 dB down, 100% FM		32	38	đΒμ	
Demodulation output	Vo	V _{IN} = 100 dBμ, 100% FM	380	560	750	mV	
Signal-to-noise ratio	S/N	$V_{IN} = 100 \text{ dB}\mu$	71	77		dB	
Amplitude modulation rejection ratio	AMR	V _{IN} = 100 dBμ, 30% AM, 1 kHz tone	48	61		dB	
Total harmonic distortion	THD	V _{IN} = 100 dBμ, 100% FM		0.2	1.0	%	
	V _{SM} 1	No input	0	0.1	0.3	%	
Signal meter output	V _{SM} 2	$V_{IN} = 70 \text{ dB}\mu$	0.7	1.2	1.8	V	
	V _{SM} 3	$V_{IN} = 100 \text{ dB}\dot{\mu}$	2.1	2.9	3.3	V	
IF buffer output	V _{IF}	V _{IN} = 50 dBμ	170	260	350	mV	
Tuner turn-on sensitivity	V _S	Variable sensitivity	Í	(59)		dΒμ	
[FM stereo: $L + R = 90\%$, pilot =	10%, $V_{IN} = 10$	00 dB μ , f _m = 1 kHz]	•				
Channel separation	Sep 1K		30	45		₫B	
Total harmonic distortion	THD main	Stereo, main		0.3	1.0	%	
Bandwidth	BW	Stereo, main	160	210	280	kHz	
Channel balance	CB	Mono AM	-1.0	0	0.1	dB	
Mute attenuation	ATT	Mono	67	82		dB	
Lamp turn-on level	Pilot	Stereo, main	1.2	3.1	4.5	%	
Lamp hysteresis	Hs	Stereo, main		(2.5)		dB	

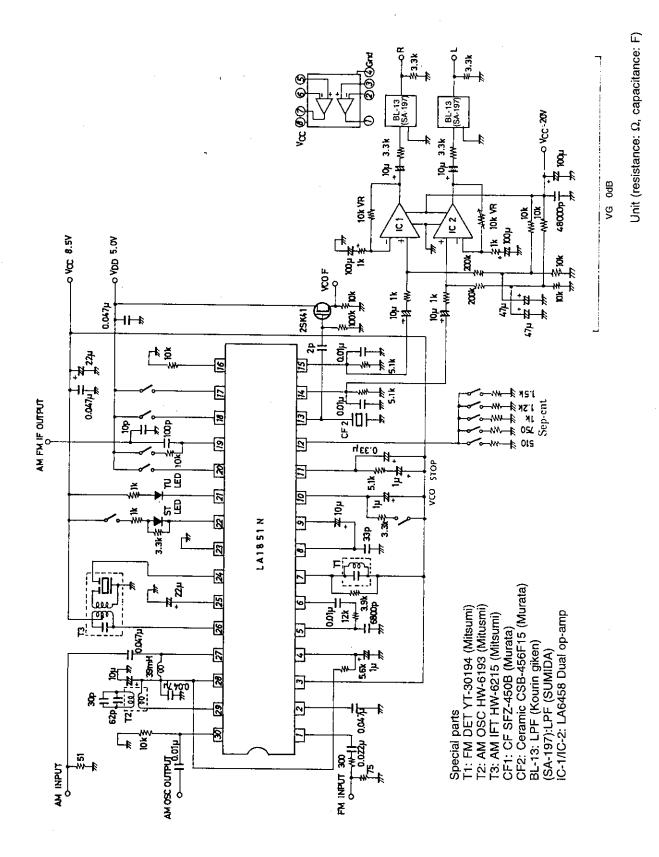
Note: Figures in parenthesis denote design guarantee values.



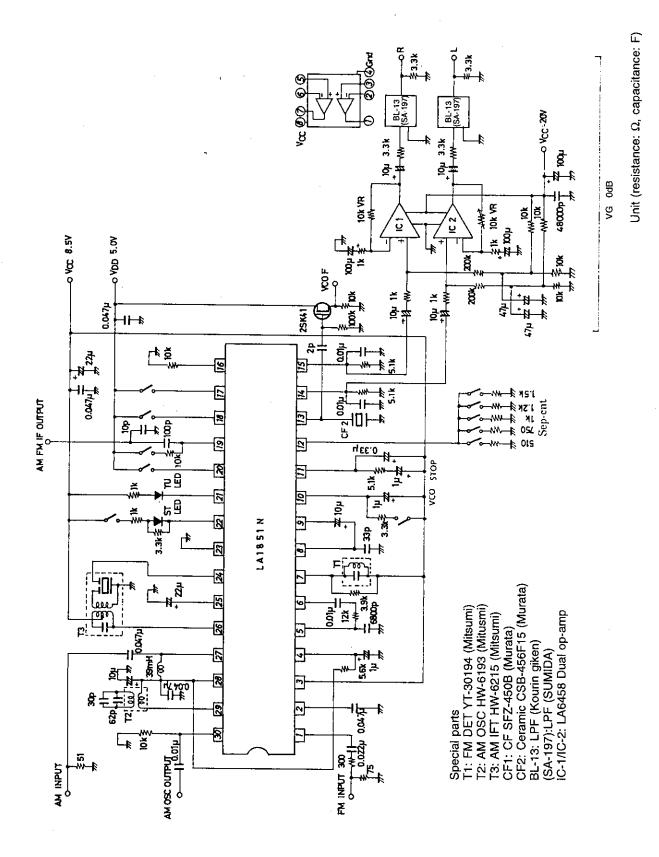
Block Diagram



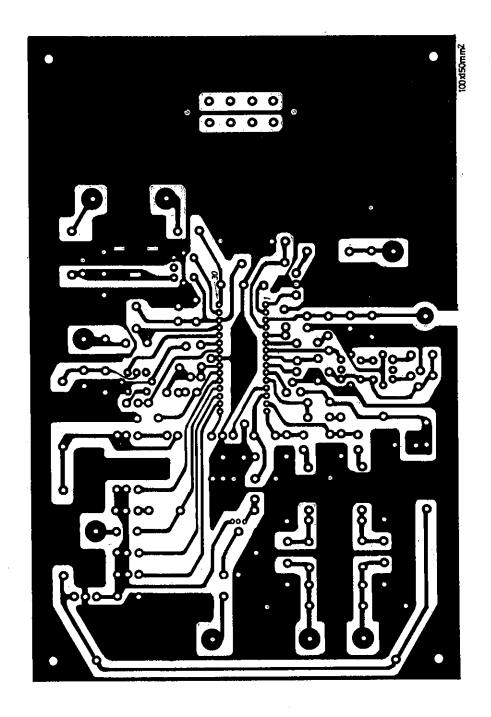
Test Circuit



Test Circuit



Printed Circuit Board Layout



Pin Functions

Pin no.	Function	Internal equivalent circuit	Remarks
1	FM IF input		Input impedance: 330 Ω
2	FM IF bias	O	
3	Vcc	③———	
4	FM AFC output	-\tag{\tag{\tag{\tag{\tag{\tag{\tag{	Forced monaural mode is established in synchronization with the extinguishing of ST LED during FM AFC detuning.
5	AM demodulation output	5	
6	MPX AM DET input	© + K	MPX block AM demodulation input pin Input impedance: 27 kΩ
7	FM discriminator output	©	
8	FM demodulation output		Output impedance: 5 kΩ
9	MPX FM DET output	9	MPX block FM demodulation input pin Input impedance: 27 k Ω
10	MPX pilot sync detection filter		MPX VCO is stopped by shorting the V10 voltage in the V3 V _{CC} line. However, a 3.3 k Ω current-limiting resistor is required.
11	MPX PLL loop filter		

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LA1851N

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Pin no.	Function	Internal equivalent circuit	Remarks
12	MPX Separation control	(B)	
13	MPX VCO		Ceramic oscillator connection pin. CSB456F15 (Murata) recommended
14	MPX Left channel output		
15	MPX Right channel output		
16	AM SD ADJ	8	
17	MPX AF muting drive	0 m	V _{HI} (≧1.5 V): Mute on V _{LO} (<1.5 V): Mute off (μ-COM direct control enabled)
18	AM/FM switch	***	V _{HI} (≧ 1.5V): FM V _{LO} (< 1.5V): AM (μ-COM direct control enabled)
19	AM/FM IF counter output/ switch		V _{HI} (≧1.5 V): IF CNT ON V _{LO} (<1.5 V): IF CNT OFF (μ-COM direct control enabled)
2 0	Forced TU/ST LED extinguishing drive pin	∞ ***	V _H i (≧1.5 V): LED forced OFF (forced monaural mode) V _{LO} (<1.5 V): Normal (μ-COM direct control enabled)
21	AM/FM TU LED	® \	
22	MPX ST LED	*	

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Number	Symbol	Equivalent circuit	Description
23	AM/FM MPX GND	GND 23 777	
24	AM IF input	****	Input impedance: 2 kΩ
25	AM AGC output - FM S-meter output	S	
26	AM MIXER output	*	
27	AM RF input	@ 	
28	V Reg	**	Vreg = 2.3 V
29	AM OSC		
30	AM OSC buffer output/ FM SD ADJ	•	

